

WHAT IS CLAIMED IS:

Sub  
a1  
1. A disk device comprising:

a disk drive including a head for reading data written to a disk and a processing circuit for processing the data; and

a host computer connected to said disk drive through an interface;

wherein the processing circuit of said disk drive includes a low-level error-correction unit for performing error correction of the data written to the disk in units of one block; and

the host computer includes a high-level error-correction unit for performing error correction of the read data supplied through the interface in units of a plurality of blocks.

Sub  
B1  
2. A disk device according to Claim 1, wherein:

a high-reliability disk to which both a low-level error-correction code and a high-level error-correction code are written and a disk to which only the low-level error-correction code is written are loadable into said disk drive;

when the high-reliability disk is loaded, the processing circuit of said disk drive performs low-level

error correction; and then said host computer, to which the corrected data is supplied, performs high-level error correction; and

B1 when the latter disk is loaded, the processing circuit of said disk drive performs low-level error correction, and said host computer processes the corrected data.

3. A disk device according to Claim 1, wherein:

information is written to the disk for discriminating a high-reliability disk to which both a low-level error-correction code and a high-level error-correction code are written from a disk to which only the low-level error correction is written; and

said host computer determines which disk is inserted based on the information.

add  
a2